

DEC 09 2004

5098383424

P.01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/784,074
Confirmation No. 1159
Filing Date February 20, 2004
Inventor Kyle K. Kirby
Assignee Micron Technology, Inc.
Group Art Unit 2823
Examiner William M. Brewster
Attorney's Docket No. MI22-2401
Customer No. 021567
Title: Methods of Fabricating Interconnects for Semiconductor Components

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR § 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9306 on the date shown below:

1. Certificate of Facsimile Transmission
2. Response to December 3, 2004 Office Action

Dated: December 9, 2004By: 

Jennifer Gaines, Legal Assistant

Telephone No.

(509) 624-4276

Facsimile No.

(509) 838-3424

FEE DEFICIENCY

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Dated: December 9, 2004By: 

David G. Latwesen, Ph.D.

Reg. No. 38,533

NUMBER OF PAGES IN FACSIMILE: 8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.10/784,074
Confirmation No.1159
Filing Date February 20, 2004
Inventor..... Kyle K. Kirby
Assignee Micron Technology, Inc.
Group Art Unit.....2823
Examiner William M. Brewster
Attorney's Docket No.MI22-2401
Customer No.021567
Title: Methods of Fabricating Interconnects for Semiconductor Components

RESPONSE TO DECEMBER 03, 2004 OFFICE ACTION

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: David G. Latwesen, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

AMENDMENTS

S:\MI22\2401\m01.doc